

National Exam December 2016

07-Elec-A1, Circuits

3 hours duration

NOTES:

1. **No questions to be asked.** If doubt exists as to the interpretation of any question, the candidate is urged to submit with the answer paper, a clear statement of any logical assumptions made.
2. One of two calculators is permitted; any Casio or Sharp approved model.
3. This is a **closed book** examination.
4. Any **five questions** constitute a complete paper. Please **indicate in the front page of your answer book which questions you want to be marked.** If not indicated, only the first five questions as they appear in your answer book will be marked.
5. All questions are of equal value. **Part marks will be given for right procedures.**
6. **Some useful equations and transforms** are given in the last page of this question paper.

Q1: (a) In the circuit shown in Figure-1, calculate the equivalent resistance at terminals a-b, R_{ab} . [10]

(b) Calculate the voltage, V across the 20Ω resistance. [10]

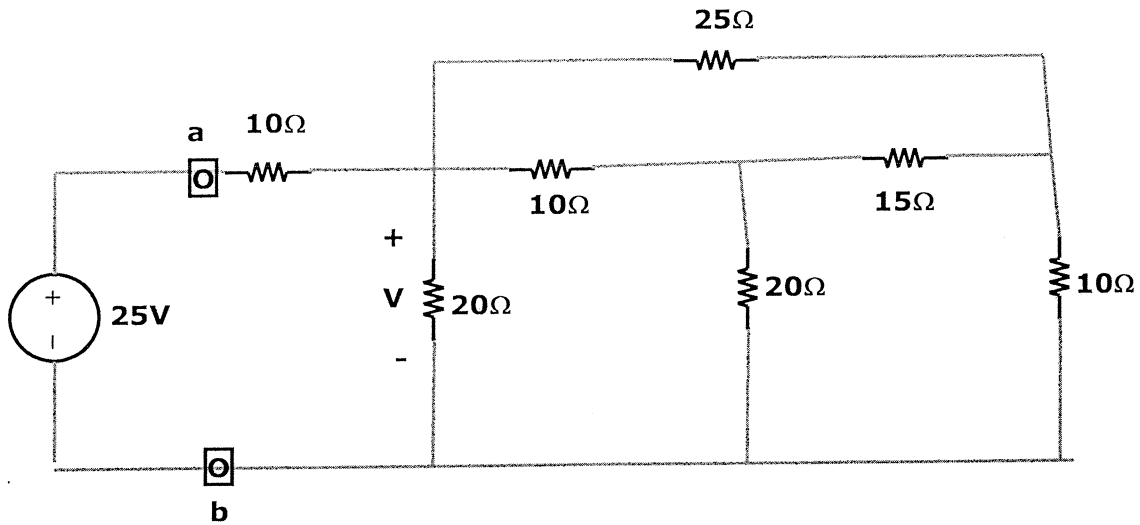


Figure-1

Q2: (a) Write the mesh current equations of the circuit shown in Figure-2. [12]

(b) Solve the current, I from the $20V$ voltage source. [8]

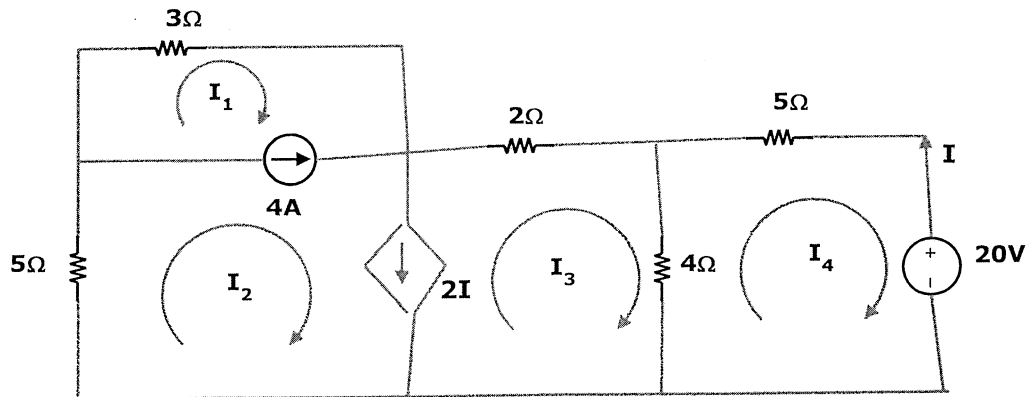


Figure-2

Q3: For the Circuit shown in Figure-3, the switch was initially closed. At $t=0$, the switch is opened. **Note** the voltage, $20u(t) = 0$ at $t < 0$, and $20u(t) = 20V$ at $t > 0$.

- (a) Solve $V_c(0^-)$ and $i(0^-)$, i.e just before the switch was opened. [4+4]
 (b) Solve $V_c(t)$ and $i(t)$ at $t > 0$, i.e after the switch was opened. [6+6]

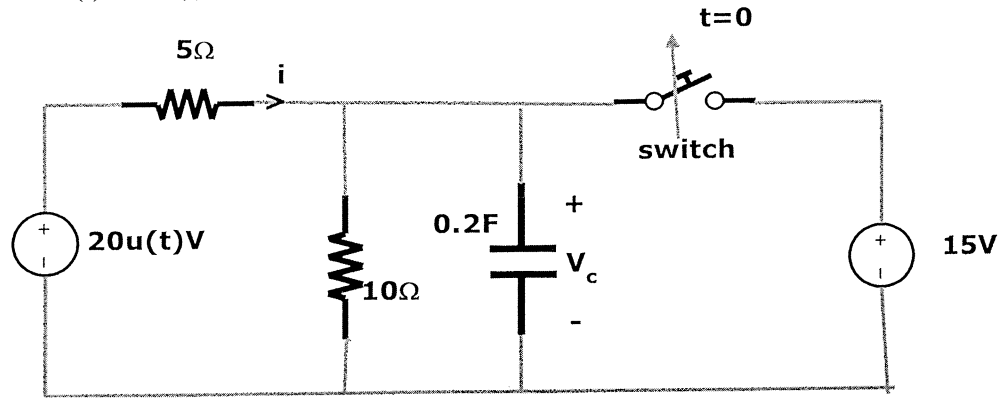


Figure-3

- Q4:** (a) In the circuit shown in Figure-4, draw the phasor form of the circuit, and write the Node voltage equations with respect to the reference(ground) as indicated. [12]
 (b) Solve node voltages, and calculate the branch current $i(t)$ through the capacitor. [8]

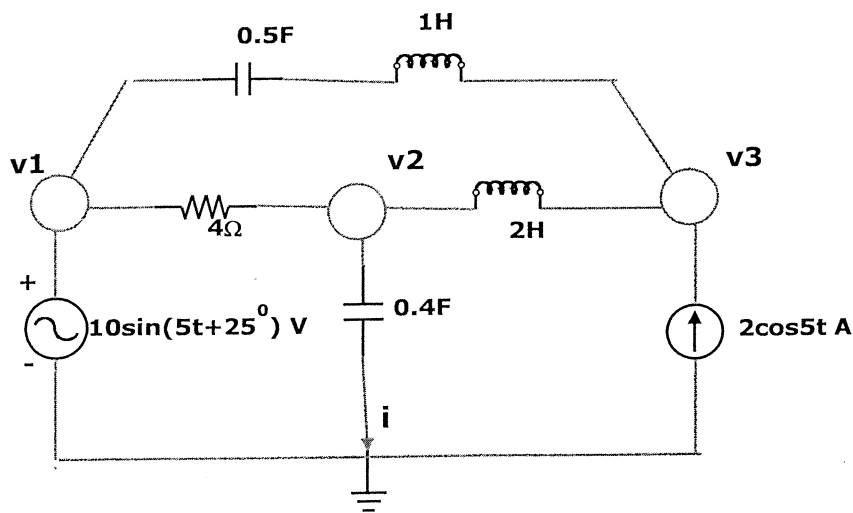


Figure-4

- Q5:** (a) Calculate the Thevenin's Voltage, V_{th} and Thevenin's impedance, Z_{th} at the terminals **a-b** of the circuit shown in Figure-5. [6+6]
- (b) What value of load impedance Z_L which can be connected at terminals **a-b** for maximum power dissipation in Z_L ? [4]
- (c) Calculate the maximum power, P_{max} which can be dissipated in Z_L . [4]

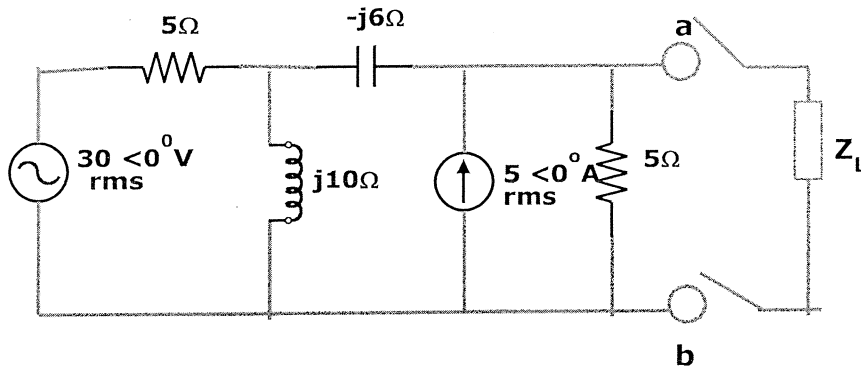


Figure-5

- Q6:** (a) Convert the circuit shown in Figure-6 to its Laplace equivalent. [10]
- (b) Solve the output voltage $v(t)$ in the time domain, at $t \geq 0$. [10]

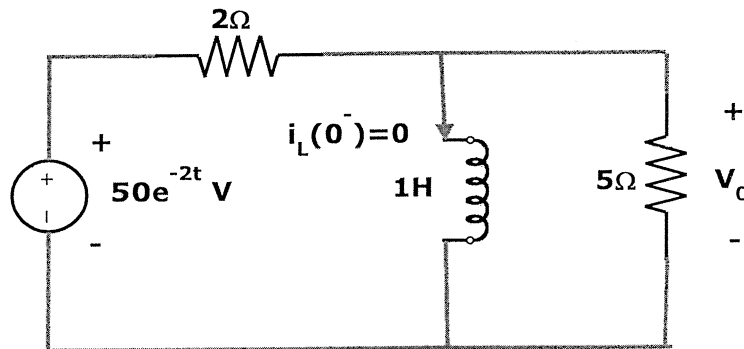


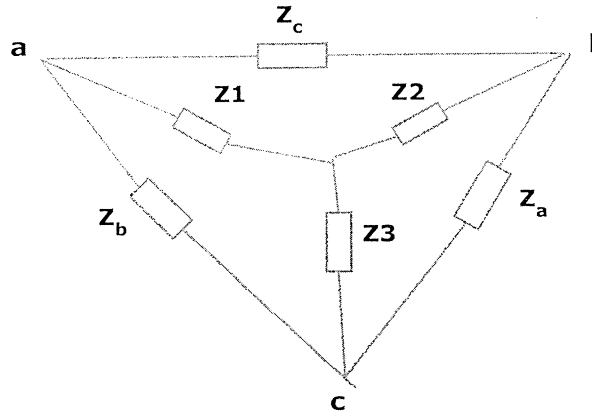
Figure-6

Appendix

Some useful Laplace Transforms:

<u>f(t)</u>	→	<u>F(s)</u>
$Ku(t)$		K/s
$\delta(t)$		1
t		$1/s^2$
$e^{-at} u(t)$		$1/(s+a)$
$\sin \omega t \cdot u(t)$		$\omega / (s^2 + \omega^2)$
$\cos \omega t \cdot u(t)$		$s / (s^2 + \omega^2)$
$e^{-at} \sin \omega t$		$\frac{\omega}{(s+\alpha)^2 + \omega^2}$
$e^{-at} \cos \omega t$		$\frac{(s+\alpha)}{(s+\alpha)^2 + \omega^2}$
$\frac{df(t)}{dt}$		$sF(s) - f(0^-)$
$\frac{d^2 f(t)}{dt^2}$		$s^2F(s) - sf(0^-) - f'(0^-)$
$\int_{-\infty}^t f(q) dq$		$\frac{F(s)}{s} + \int_{-\infty}^0 f(q) dq$

Star – Delta conversion:



$$Z_1 = \frac{Z_b \cdot Z_c}{Z_a + Z_b + Z_c}$$

$$Z_2 = \frac{Z_a \cdot Z_c}{Z_a + Z_b + Z_c}$$

$$Z_3 = \frac{Z_a \cdot Z_b}{Z_a + Z_b + Z_c}$$

$$Z_a = \frac{Z_1 \cdot Z_2 + Z_2 \cdot Z_3 + Z_3 \cdot Z_1}{Z_3}$$

$$Z_b = \frac{Z_1 \cdot Z_2 + Z_2 \cdot Z_3 + Z_3 \cdot Z_1}{Z_2}$$

$$Z_c = \frac{Z_1 \cdot Z_2 + Z_2 \cdot Z_3 + Z_3 \cdot Z_1}{Z_1}$$